Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1.   (previously presented) A method comprising:

displaying on a display device a first graphic type indicative of a processor usage for each one of at least two processors in a multiprocessor system, the processor usage including at least one of processor assignment, processor availability, and clustering; and

displaying on said display device a second graphic type indicative of an application assignment for each one of at least two application groups associated with each of said at least two processors.

2.   (previously presented) The method as recited in claim 1 wherein said second graphic type comprises a color indicative of the application assignment for each one of the at least two application groups.

3.   (previously presented) The method as recited in claim 1 further comprising a graphic indicator indicating a group of said at least two processors wherein said group is indicative of a processor clustering.

4.   (previously presented) The method as recited in claim 1 further comprising a graphic indicator of processor utilization associated with each of said at least two processors.

5.   (original) The method as recited in claim 4 wherein said graphic indicator comprises a gauge.

6.   (previously presented) The method as recited in claim 5 wherein the gauge includes bands that reflect ranges of processor utilization.

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7. (previously presented) The method as recited in claim 4 wherein said graphic indicator comprises a bar.

8. (previously presented) The method as recited in claim 1 wherein blocks associated with the second graphic type indicate an application group assigned to a processor.

9. (previously presented) The method as recited in claim 1 wherein each one of the at least two application groups comprises at least one independently, computer-executable process.

10. (previously presented) An apparatus comprising:
a memory;
a display device in electrical communication with a multiprocessor system having at least two processors;
computer-executable instructions stored in said memory and operable to display on said display device a first graphic type indicative of a processor usage for each one of the at least two processors, the processor usage including at least one of processor assignment, processor availability, and clustering; and
computer-executable instructions stored in said memory and operable to display on said display device a second graphic type indicative of an application assignment for each one of at least two application groups associated with each of said at least two processors.

11. (previously presented) The apparatus as recited in claim 10 wherein said second graphic type comprises a color indicative of the application assignment for each one of the at least two application groups.

12. (previously presented) The apparatus as recited in claim 10 further comprising a graphic indicator indicating a group of said at least two processors wherein said group is indicative of a processor clustering.

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13. (previously presented) The apparatus as recited in claim 12 further comprising a graphic indicator of processor utilization associated with each of said at least two processors.

14. (original) The apparatus as recited in claim 13 wherein said graphic indicator comprises a gauge.

15. (previously presented) The apparatus as recited in claim 14 wherein the gauge includes bands that reflect ranges of processor utilization.

16. (previously presented) The apparatus as recited in claim 13 wherein said graphic indicator comprises a bar.

17. (previously presented) The apparatus as recited in claim 10 wherein blocks associated with the second graphic type indicate an application group assigned to a processor.

18. (previously presented) The apparatus as recited in claim 10 wherein each one of the at least two application groups comprises at least one independently, computer-executable process.

19. (previously presented) A computer-readable medium bearing computer readable instructions for carrying out the acts comprising:

displaying on a display device a first graphic type indicative of a processor usage for each one of at least two processors in a multiprocessor system, the processor usage including at least one of processor assignment, processor availability, and clustering; and

displaying on said display device a second graphic type indicative of an application assignment for each one of at least two application groups associated with each of said at least two processors.

20. (previously presented) The computer readable medium as recited in claim 19 wherein said second graphic type comprises a color indicative of the application assignment for each one of the at least two application groups.

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21.   (previously presented) The computer-readable medium as recited in claim 19 further comprising a graphic indicator indicating a group of said at least two processors wherein said group is indicative of a processor clustering.

22.   (previously presented) The computer-readable medium as recited in claim 19 further comprising a graphic indicator of processor utilization associated with each of said at least two processors.

23.   (original) The computer-readable medium as recited in claim 22 wherein said graphic indicator comprises a gauge.

24.   (original) The computer-readable medium as recited in claim 23 wherein said gauge comprises bands that reflect ranges of processor utilization.

25.   (previously presented) The computer-readable medium as recited in claim 22 wherein said graphic indicator comprises a bar.

26.   (previously presented) The computer-readable medium as recited in claim 19 wherein blocks associated with the second graphic type indicate an application group assigned to a processor.

27.   (previously presented) The computer-readable medium as recited in claim 19 wherein each one of the at least two application groups comprises at least one independently, computer-executable process.